



B) 93 ITFW

TRANSMITTAL OF FORMAL DRAWINGS

Docket No.

In Re Application Of: **Hirokazu Hayashi**

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/059,176	January 31, 2002	A. Saxena	20987	2128	7695

METHOD FOR MODELING SEMICONDUCTOR DEVICE PROCESS

Address to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Transmitted herewith are:

9 sheets of formal drawing(s) for this application.

Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).



Dr. J. H. D.

Signature

**ANDREW J. TELESZ, JR.
REG. NO. 33,581**

**VOLENTE FRANCOS & WHITT, P.L.L.C.
11951 FREEDOM DRIVE, SUITE 1260
RESTON, VA 20190
TEL. NO. (571) 283-0720**

Dated: December 27, 2006

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on

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Signature of Person Mailing Correspondence

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